

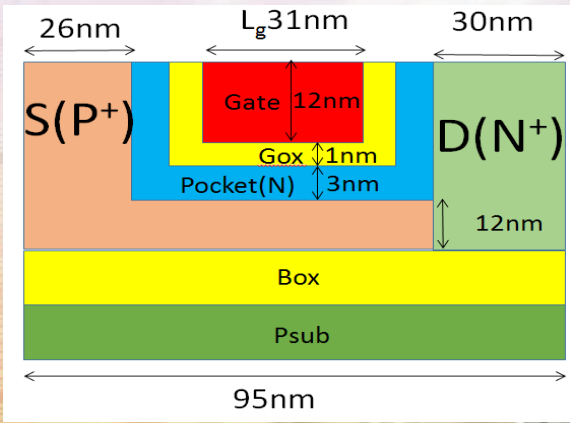
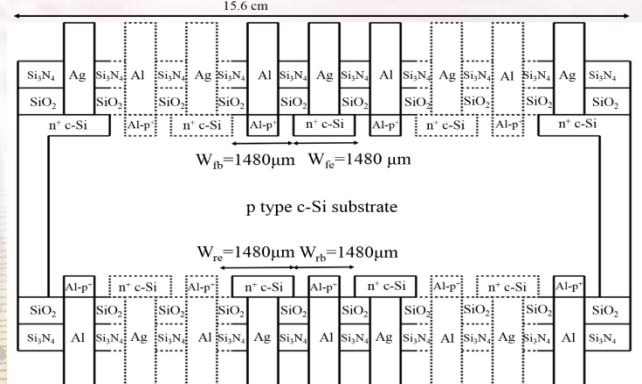
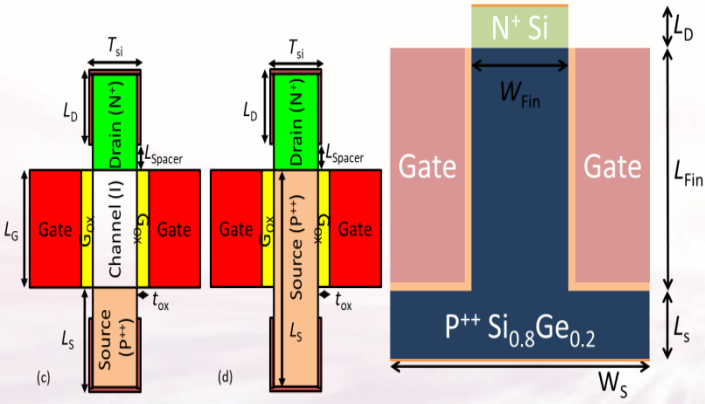
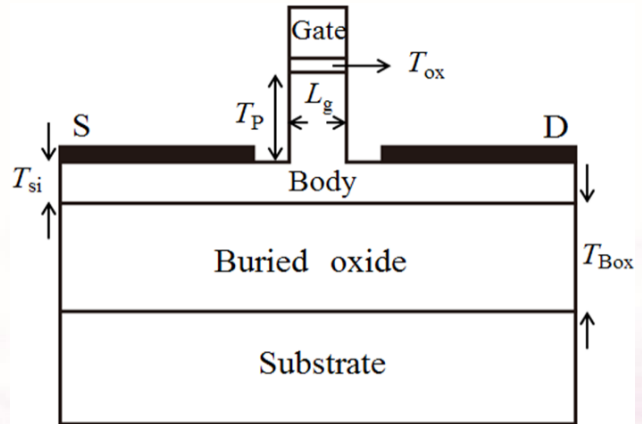
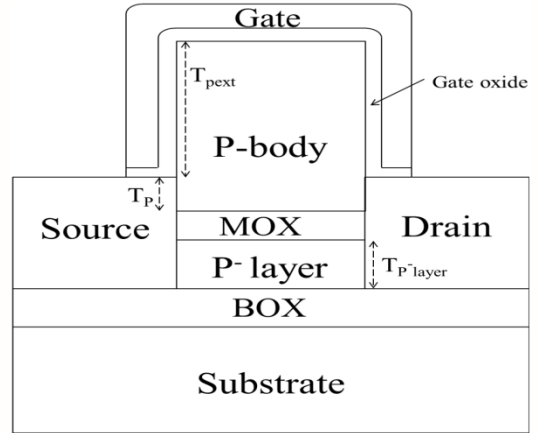
## Silicon On Insulator Laboratory

### Main Research

- i-TFET CMOS devices application
- Unipolar CMOS devices application
- Punch Through CMOS devices application
- Capacitorless 1T DRAM
- High Efficiency Solar cell

### Highlight

- Design novel CMOS devices for 5G or 6G technology application.
- Ultra low power devices.



crossed	0	39.91	0.68	82.26	22.46	0
Solar	30	51.75	0.68	82.78	29.47	31.20
Cell	100	79.91	0.70	82.83	46.44	106.72